

DESIGNATION	QTY	DESCRIPTION
R6	1	10Ω, ±5% resistor (0805)
U1	1	DS80C400 Network Microcontroller
U2, U3	2	HM62W8512BLTT-7UL 512kB RAM
U4	1	AM29LV081B-70EC 1MB flash
U5, U6	2	MAX6365PKA31 reset controllers

DESIGNATION	QTY	DESCRIPTION
U7	1	MAX1792EQU18 voltage reg
U8	1	DS1672U-33 real-time clock
U9	1	DS2502-E48 IEEE MAC address
Y1	1	14.7456MHz crystal
Y2	1	32kHz crystal

QUICK START

Recommended Equipment

It is highly recommended that the developer use the DSTINIs400 sockets board in conjunction with the DSTINIm400. The DSTINIs400 was specifically designed to hold the DSTINIm400 and provide the physical interconnects (Ethernet PHY), as well as the 1-Wire, I²C™, SPI™, CAN, and four serial port connectors.

If the developer wishes to implement his or her own design using the DSTINIm400, a 144-pin SODIMM connector such as the Hirose SX6E-144S-0.8SH is required.

FOR MORE INFORMATION

TINI platform details can be found at www.maxim-ic.com/TINI. The *TINI Specification and Developer's Guide* (Addison-Wesley, 2001) is an invaluable resource when developing with the TINI platform. Download a free copy from our website at www.maxim-ic.com/TINIGuide.

DS80C400 INFORMATION

For more information about the DS80C400 network microcontroller, please consult the DS80C400 data sheet available on our website at www.maxim-ic.com/microcontrollers.

ADDITIONAL RESOURCES

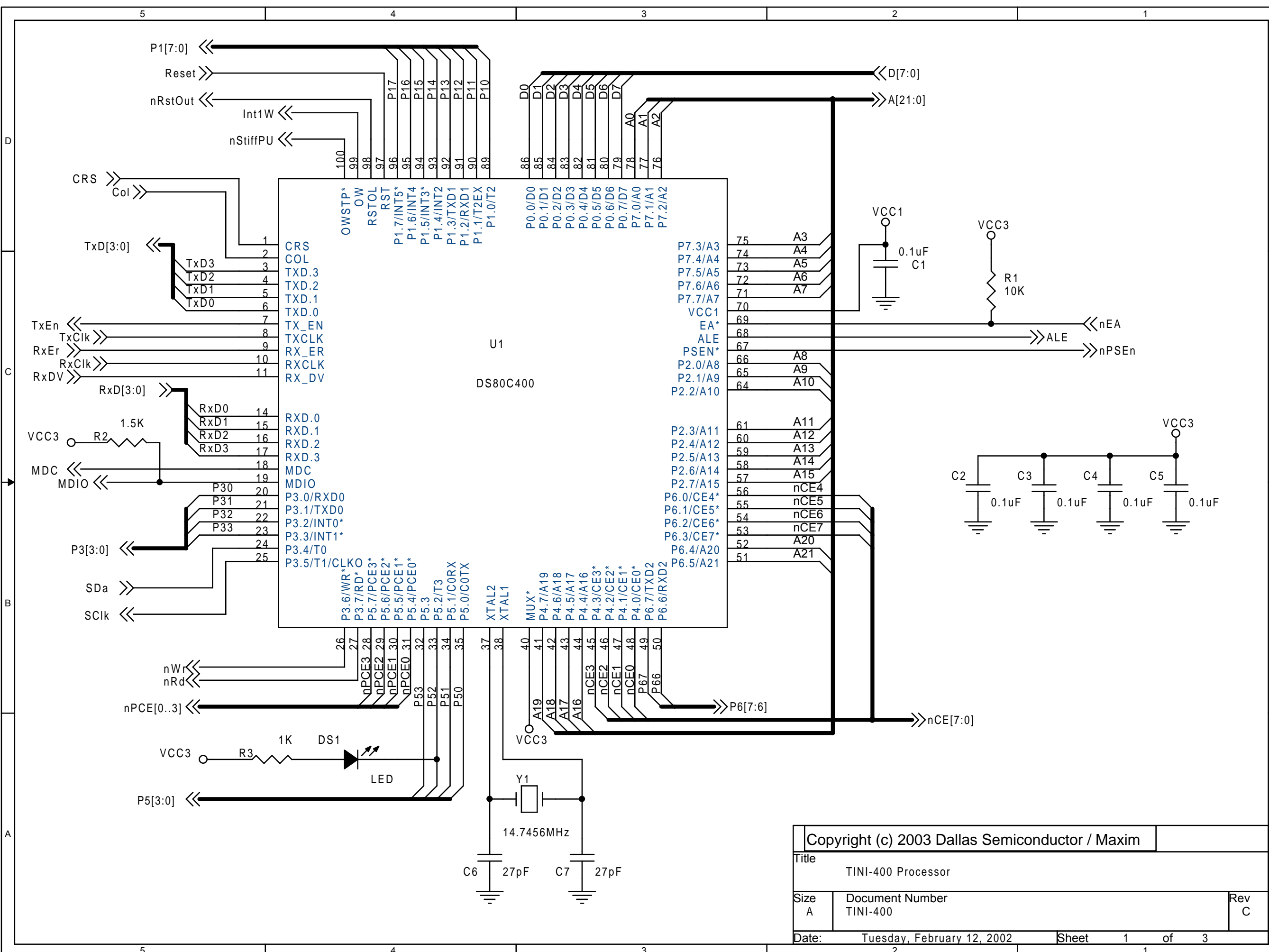
For detailed information about the initial setup and use of the TINI platform, refer to *Application Note 612: Getting Started with the TINIm400 (DS80C400) Verification Module* at www.maxim-ic.com/appnoteindex.

DS80C400 Network Microcontroller Data Sheet: www.maxim-ic.com/DS80C400

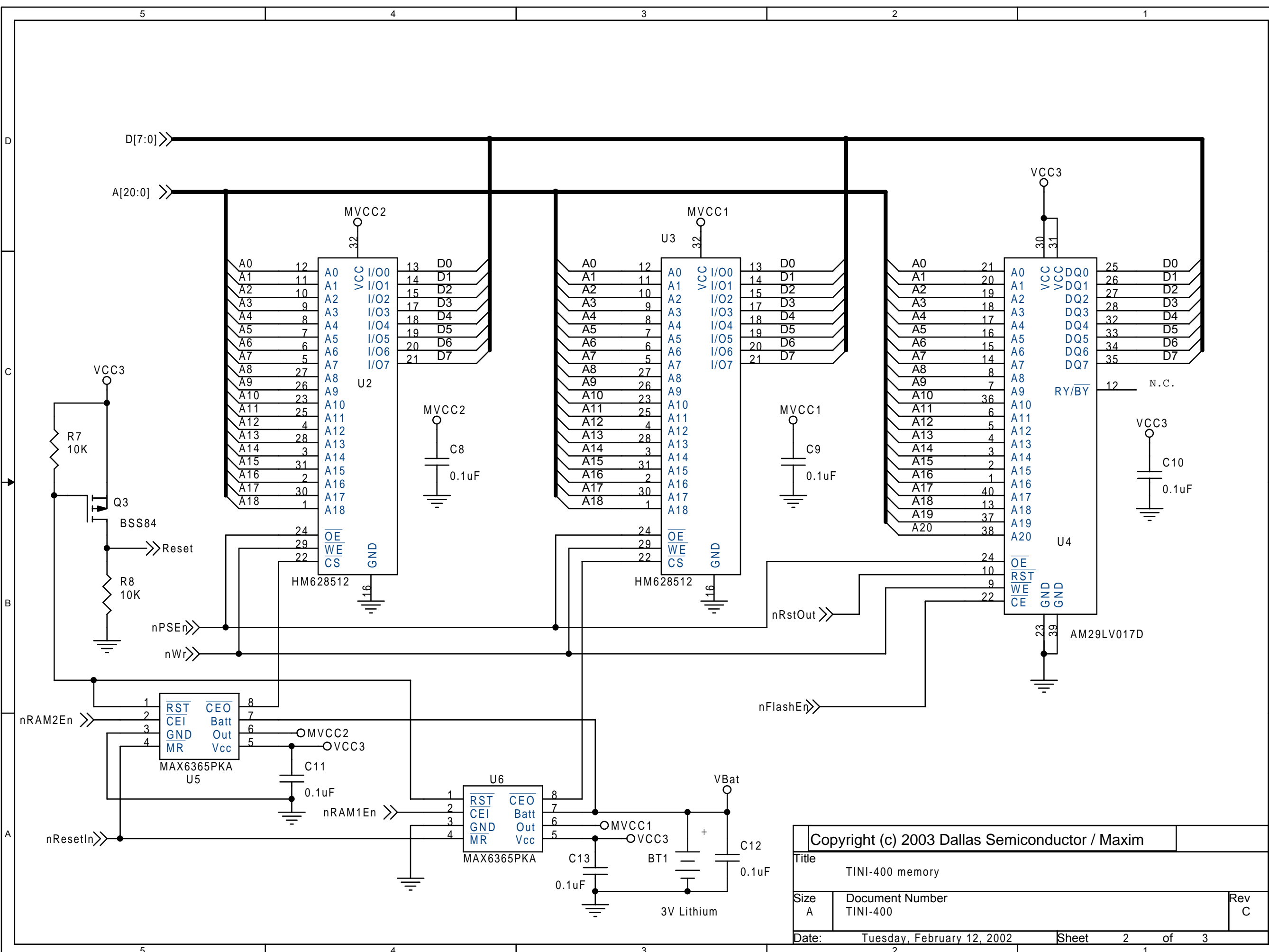
Microcontroller Website: www.maxim-ic.com/microcontrollers

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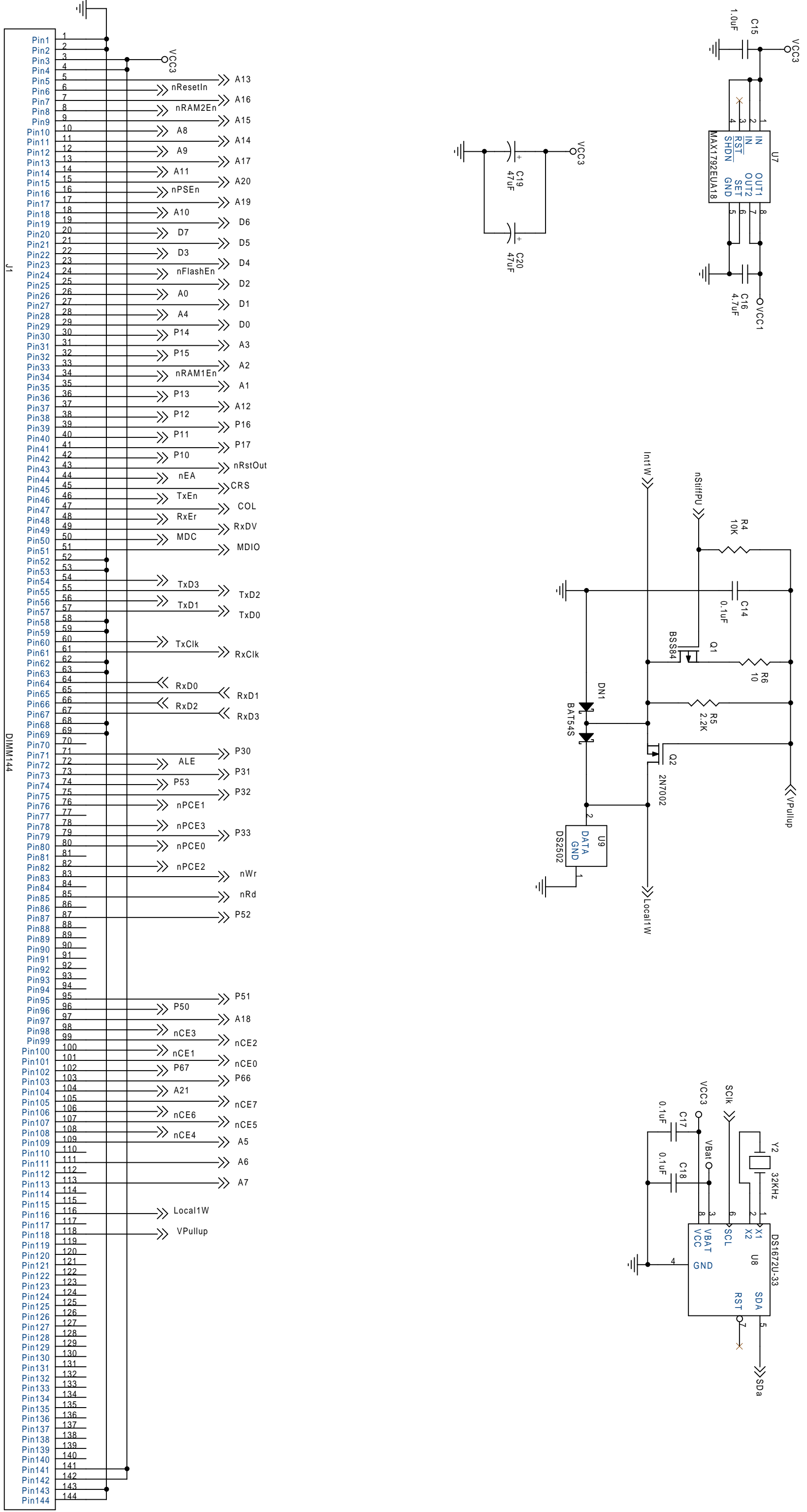
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Title TINI-400 Processor		
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Title TINI-400 memory		
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Pin1	1		
Pin2	2		
Pin3	3	VCC3	
Pin4	4		
Pin5	5		A13
Pin6	6	nResetIn	A16
Pin7	7		
Pin8	8	nRAM2En	A15
Pin9	9		
Pin10	10	A8	A14
Pin11	11		
Pin12	12	A9	A17
Pin13	13		
Pin14	14	A11	A20
Pin15	15	nPSEn	A19
Pin16	16		
Pin17	17	A10	D6
Pin18	18		
Pin19	19	D7	D5
Pin20	20		
Pin21	21	D3	D4
Pin22	22		
Pin23	23	nFlashEn	D2
Pin24	24		
Pin25	25	A0	D1
Pin26	26		
Pin27	27	A4	D0
Pin28	28		
Pin29	29	P14	A3
Pin30	30		
Pin31	31	P15	A2
Pin32	32		
Pin33	33	nRAM1En	A1
Pin34	34		
Pin35	35	P13	A12
Pin36	36		
Pin37	37	P12	P16
Pin38	38		
Pin39	39	P11	P17
Pin40	40		
Pin41	41	P10	nRstOut
Pin42	42		
Pin43	43	nEA	CRS
Pin44	44		
Pin45	45	TxEr	COL
Pin46	46		
Pin47	47	RxEr	RxDV
Pin48	48		
Pin49	49	MDC	MDIO
Pin50	50		
Pin51	51		
Pin52	52		
Pin53	53		
Pin54	54		
Pin55	55	TxD3	TxD2
Pin56	56		
Pin57	57	TxD1	TxD0
Pin58	58		
Pin59	59		
Pin60	60	TxCik	RxCik
Pin61	61		
Pin62	62		
Pin63	63		
Pin64	64	RxD0	RxD1
Pin65	65		
Pin66	66	RxD2	RxD3
Pin67	67		
Pin68	68		
Pin69	69		
Pin70	70		
Pin71	71	P30	P31
Pin72	72	ALE	P32
Pin73	73		
Pin74	74	P53	
Pin75	75		
Pin76	76	nPCE1	P33
Pin77	77		
Pin78	78	nPCE3	
Pin79	79		
Pin80	80	nPCE0	nWr
Pin81	81		
Pin82	82	nPCE2	nRd
Pin83	83		
Pin84	84		
Pin85	85		
Pin86	86		
Pin87	87		
Pin88	88		
Pin89	89		
Pin90	90		
Pin91	91		
Pin92	92		
Pin93	93		
Pin94	94		
Pin95	95	P51	
Pin96	96		
Pin97	97	P50	A18
Pin98	98		
Pin99	99	nCE3	nCE2
Pin100	100		
Pin101	101	nCE1	nCE0
Pin102	102		
Pin103	103	P67	P66
Pin104	104		
Pin105	105	A21	nCE7
Pin106	106		
Pin107	107	nCE6	nCE5
Pin108	108		
Pin109	109	nCE4	A5
Pin110	110		
Pin111	111		
Pin112	112		
Pin113	113		
Pin114	114		
Pin115	115		
Pin116	116	Local1W	
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Pin118	118	VPullup	
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Pin144	144		

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